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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Nguyen, Viet

Patent No.: 7,196,929

Group Art Unit: 2827

Issue Date: March 27, 2007

Docket No: 303.354US2

Title: METHOD FOR OPERATING A MEMORY DEVICE HAVING AN AMORPHOUS SILICON  
CARBIDE GATE INSULATOR

Commissioner for Patents  
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Alexandria, VA 22313-1450  
ATTN: CERTIFICATE OF CORRECTION BRANCH

Certificate  
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of Correction

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

**Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.**

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer No: 21186

By: David R. Cochran  
Name: David R. Cochran  
Reg. No. 46,632  
DRC/cmb

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 25 day of April 2007.

CHRIS BARNETT  
Name

Chris Barnett  
Signature

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Patent 7,196,929

PATENT

**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

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Confirmation No.: 9295

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It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

**As the error is that of the Patent Office, it is believed that no fee is due.**

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6900

Date : 25 April 2007

By: \_\_\_\_\_

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CHRIS BARTV.  
Name

C. B. Bartv.  
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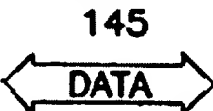
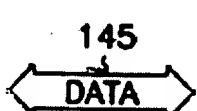
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In column 1, line 23, after "by" delete ",".

In column 1, line 52, after "typically" insert - - requires - -.

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In column 7, line 41, delete "transposing" and insert - - transporting - -, therefor.

In column 7, line 55, delete "=" and insert - -  $\approx$  -, therefor.

In column 8, line 1, after "energy, such as" delete "such as". (Second Occurrence)

In column 8, line 1, delete "=" and insert - -  $\approx$  -, therefor.

In column 8, line 32, delete "=" and insert - -  $\approx$  -, therefor.

In column 8, line 33, delete "=" and insert - -  $\approx$  -, therefor.

In column 8, line 35, after " $\Phi_{GI}$ " delete "=" and insert - -  $\approx$  -, therefor.

In column 8, line 35, after "A=1.76" delete "=" and insert - - x - -, therefor.

In column 8, line 50, delete "=" and insert - -  $\approx$  -, therefor.

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In column 9, line 7, delete "change" and insert - - charge - -, therefor.

In column 9, line 35, after "220" delete "." and insert - - , - -, therefor.

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In column 10, line 64, delete "=" and insert - -  $\approx$  -, therefor.

In column 11, line 16, delete "premitIALIZED" and insert - - preinitialized - -, therefor.

In column 12, line 23, delete "Sic" and insert - - SiC - -, therefor.

In column 12, line 37, delete "=" and insert - -  $\approx$  -, therefor.

In column 13, line 21, delete "=" and insert - -  $\approx$  -, therefor.

In column 13, line 34, after "SiO<sub>2</sub>" insert - - . - -.

In column 13, line 63, in Claim 1, after "and" insert - - a - -.

In column 14, line 13, in Claim 4, delete "following" and insert - - floating - -, therefor.

In column 14, line 64, in Claim 13, delete "calim" and insert - - claim - -, therefor.

In column 16, line 12, in Claim 25, delete "o" and insert - - a - -, therefor.

In column 16, line 13, in Claim 25, after "electrode" delete "." and insert - - in the floating gate transistor. - -, therefor.

In column 18, line 29, in Claim 42, after "gate" insert - - transistor - -.

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